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inserting phase error in bitstream

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## Patents

Patents **1 - 10** on **inserting phase error in bitstream**. (0.07 seconds)

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[Issued patents](#)[Applications](#)[Skew compensating interface for operation with arbitrary data](#)

US Pat. 6704890 - Filed Jan 29, 2001 - Nortel Networks Limited

... to a sampling sequence to form a reference **bitstream**; 5 **inserting** said n ...  
**bitstream** to indicate to the transmit side any detected **phase error**. 58. ...[Synchronizing system for spread spectrum transmissions between small earth ...](#)

US Pat. 4617674 - Filed Jul 14, 1983 - RCA Corporation

If the **phase error** still exceeds predetermined limits, ... limits by computing the **phase error** by logic contained in MES 168 and then **inserting** ...[Method and apparatus for generation of test bitstreams and testing of close ...](#)

US Pat. 6101864 - Filed Dec 17, 1997 - I/O Sensors, Inc.

Nonetheless, this **phase shift error** is not serious and may 55 Test Pattern Generating ... 4A, their sum may be accurately represented by a single **bitstream** ...[Write format for digital data storage](#)

US Pat. 6167550 - Filed Aug 14, 1998 - Overland Data, Inc.

A bit inserter is also provided, the bit inserter **inserting** one or more bits into ... bits into data stream regions having low **phase** information content. ...[Method and circuit for built in self test of \*\*phase\*\* locked loops](#)

US Pat. 6396889 - Filed Nov 2, 1998 - LogicVision, Inc.

The mean value of the jitter is the mean **phase error** and it can be measured ... In general, the tap which corresponds to 50 the mean **phase error** will be the ...[Magnetic tape drive apparatus including a variable rate encoder](#)

US Pat. 6597526 - Filed Aug 14, 1998 - Overland Storage, Inc.

In both cases, the calculated **phase error** is typically converted into an analog voltage or current signal with a digital to analog converter. ...[Data communication method and apparatus](#)

US Pat. 4700363 - Filed Feb 10, 1986 - Devon County Council

Rather than **inserting** a pause between each 35 frame, a more economic arrangement ... The generated symbol words are fed to a **phase** modulator 48 where they ...[Pilot signal transmission technique and digital communication system using same](#)

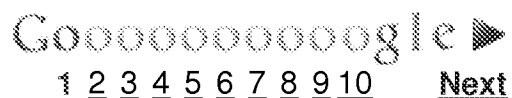
US Pat. 6608843 - Filed Apr 15, 1999 - Matsushita Electric Industrial Co., Ltd.

of information signals through a quadrature **phase** shift receiving means comprising: ... A system as defined in claim 23, wherein said **inserting** 40 maximum Me ...[Reliable ATM microwave link and network](#)

US Pat. 5648969 - Filed Feb 13, 1995 - Netro Corporation

The CU provides said <\*& decapsulation and **error** detection circuit and said ... A modulator exchange cells among themselves, converts the **bitstream** to ...[Multichannel radio frequency transmission system to deliver wideband digital ...](#)

US Pat. 6000000 - Filed Dec 15, 1998 - Motorola, Inc.



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